

Specification

Drive board model: JD567T123A+6124P

Driver board version: VER:1.00

LCD screen model: HE070NA-13B-27H

USER			MANUFACTURER		
QA	Project	Approved by	Prepared by	Checked by	Approved by

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Revision

[illegible]

1.Profile:

JD567T123A+6124P VER : 1.00-HE070NA-13B-27H color LCD module is composed of JD567T123A+6124P VER: 1.00 memory driver board and (HE070NA-13B-27H) screen. With CVBS&AHD signal and PAL & NSTC two forms , it can realize automatic identification, button to adjust color, brightness contrast, OSD menu support language for (English). It is mainly used for other display electronic devices, such as visual doorbell, videophone, building intercom and so on.

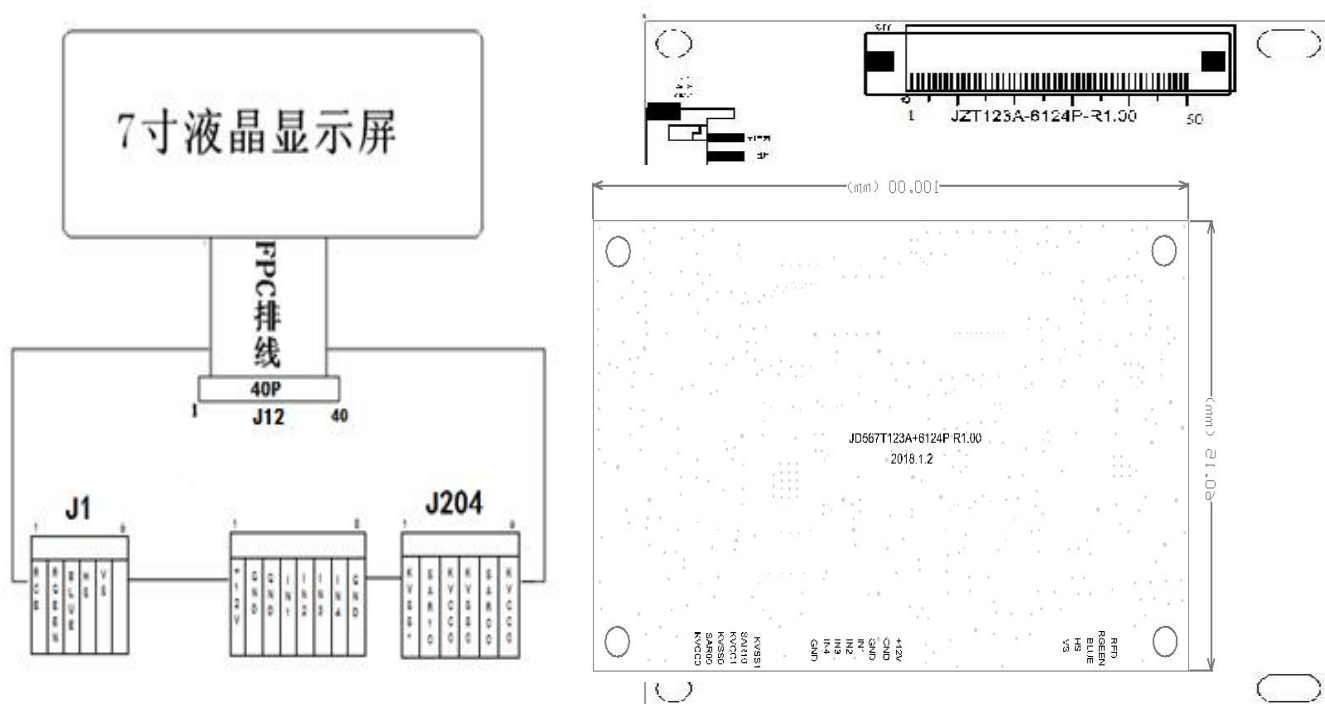
2. General Parameters:

No.	Item	Description	Remark
1	Size	7.0inch	
2	Display ratio	16:9	
3	Backlight	LED	
4	Brightness	300 cd/m ²	
5	Resolution	1024×3(RGB)×600	
6	View angle (up,down,left,right)	(70/75/75/75)	
7	Display dimension	160 (W) ×101.1 (H) ×3.0 (D) mm	
8	Effective display area	153.6 (H) ×90.0 (V) mm	
9	Driving board dimension	100.0 (W) ×60.15 (H) ×9.2 (D) mm	
10	Operating voltage (power ripple less than 0.3V _{P-P})	Min: DC9V; Standard: DC12V; Max: DC18V;	
11	Working current (DC 12V)	DC220mA±20mA	
12	Working consumption	2.64W (TYP)	
13	Start time	≤2.0s	
14	Working temperature	-10℃~60℃	
15	Storage temperature	-20℃~70℃	
16	Humidity	5~95%RH	

3、 Product picture:



4、Wiring diagram:



5、Definition of driving board interface

5.1、J103interface definition

Pin No.	Symbol	I/O	Function	Remark
1	VCOM	P	Common Voltage	
2	VDD	P	Power Voltage for digital circuit	
3	VDD	P	Power Voltage for digital circuit	
4	NC	--	No connection	
5	Reset	O	Global reset pin	
6	STBYB	O	Standby mode, Normally pulled high STBYB = "1", normal operation STBYB = "0", timing controller, source driver will turn off, all output are High-Z	
7	GND	P	Ground	
8	RXIN0-	O	-LVDS differential data input	
9	RXIN0+	O	+ LVDS differential data input	
10	GND	P	Ground	
11	RXIN1-	O	-LVDS differential data input	
12	RXIN1+	O	+ LVDS differential data input	
13	GND	P	Ground	
14	RXIN2-	O	-LVDS differential data input	
15	RXIN2+	O	+ LVDS differential data input	
16	GND	P	Ground	
17	RXCLKIN-	O	-LVDS differential clock input	
18	RXCLKIN+	O	+ LVDS differential clock input	
19	GND	P	Ground	
20	RXIN3-	O	-LVDS differential data input	
21	RXIN3+	O	+ LVDS differential data input	
22	GND	P	Ground	
23	NC	--	No connection	
24	NC	--	No connection	
25	GND	P	Ground	
26	NC	--	No connection	
27	DIMO	O	Backlight CABC controller signal output	
28	SELB	O	6bit/8bit mode select	
29	AVDD	P	Power for Analog Circuit	
30	GND	P	Ground	
31	LED-	P	LED Cathode	

32	LED-	P	LED Cathode	
33	L/R	O	Horizontal inversion	
34	U/D	O	Vertical inversion	
35	VGL	P	Gate OFF Voltage	
36	CABCEN1	O	CABC H/W enable	
37	CABCEN0	O	CABC H/W enable	
38	VGH	P	Gate ON Voltage	
39	LED+	P	LED Anode	
40	LED+	P	LED Anode	

5.2、J1interface definition:

NO.	PIN	I/O/P	Description	Remark
1	RED	I	Signal input	
2	RGREEN	I	Signal input	
3	BLUE	I	Signal input	
4	HS	I	Line signal input	
5	VS		Field signal input	

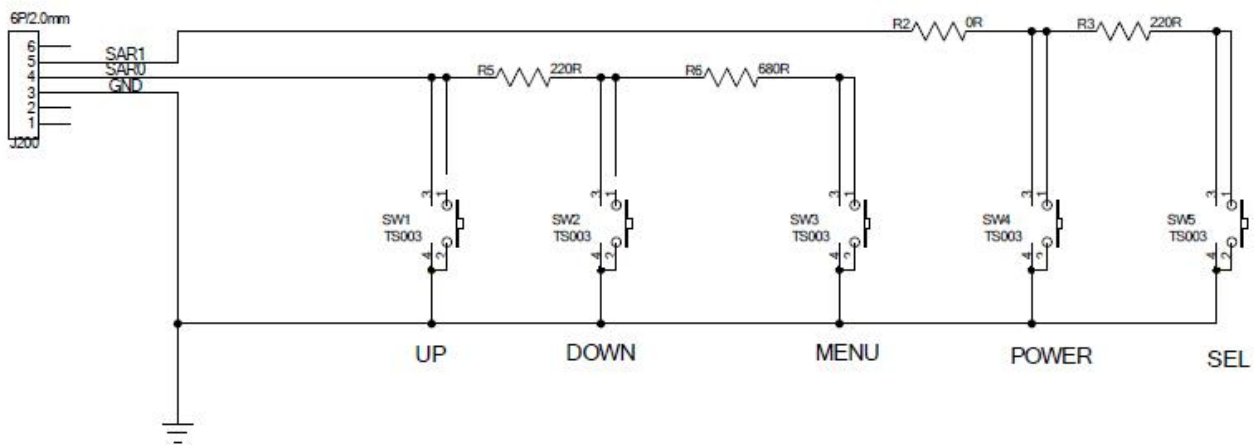
.3、J4interface definition

NO.	PIN	I/O/P	Description	Remark
1	+12V	I	+12V DC power input	9-15V
2	GND	P	GND	
3	GND	P	GND	
4	IN1	I	Signal input	CVBS+AHD
5	IN2	I	Signal input	CVBS+AHD
6	IN3	I	Signal input	CVBS+AHD
7	IN4	I	Signal input	CVBS+AHD
8	GND	P	GND	

5.4 、J204 interface definition:

NO.	PIN	I/O/P	Description	Remark
1	KVSS1	O	GND	
2	SAR10	I	Potentiometer input	
3	KVCC1	I		
4	KVSS0	P	GND	
5	SAR00	I/O	Potentiometer input	
6	KVCC0	I	Power	

5.3.1、Keyboard connecting diagrams:

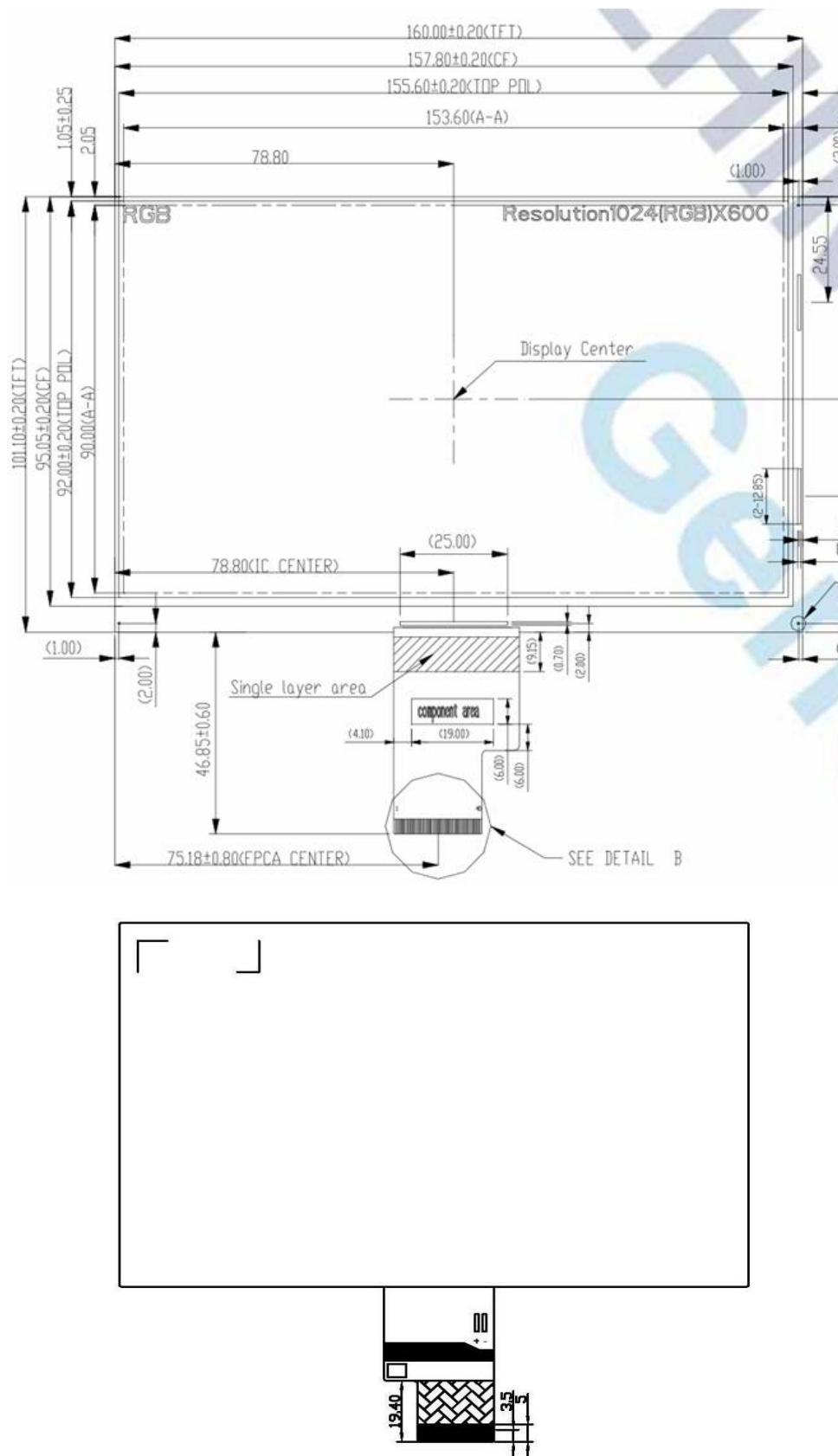


5.3.2、Keyboard sequence as shown below:

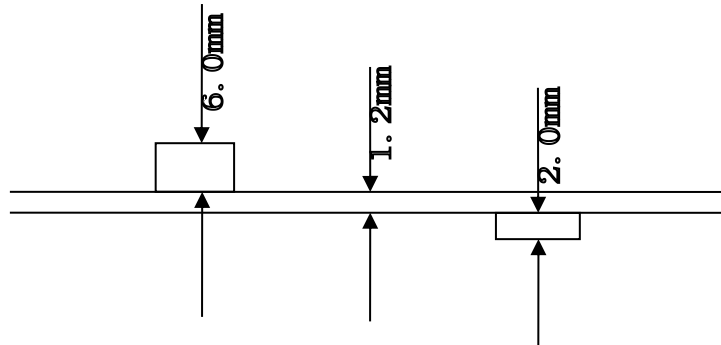
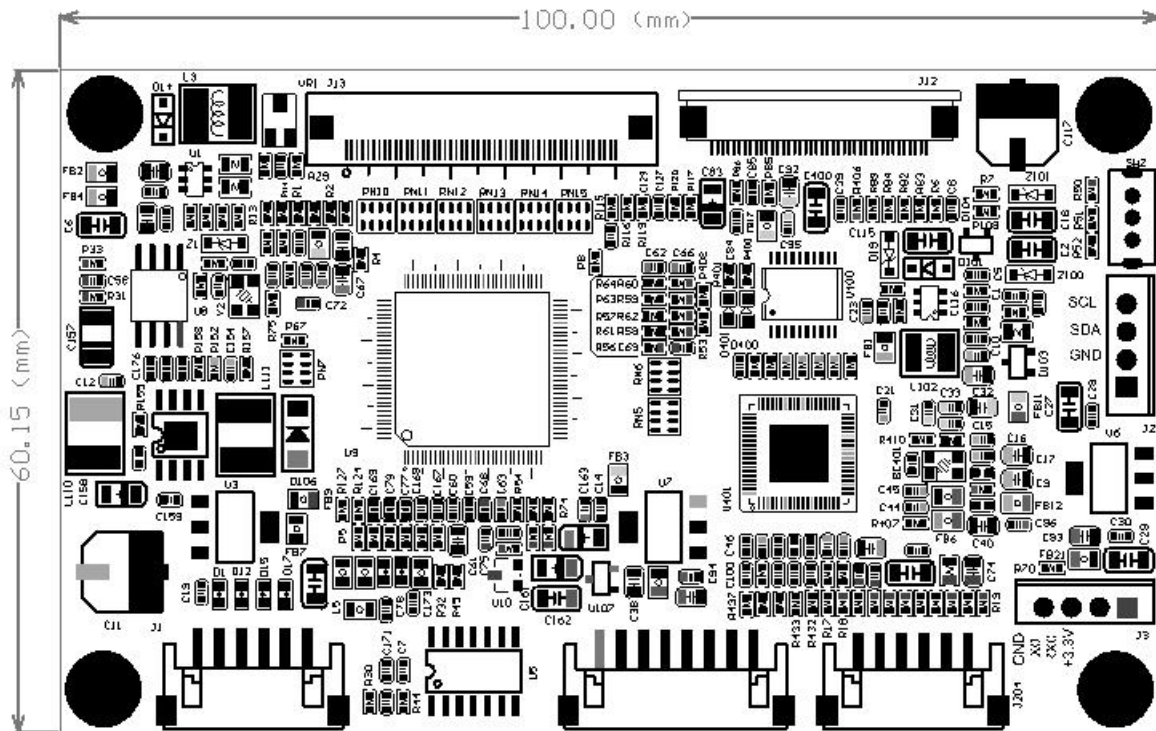


6、 Structure Diagram:

6.1、 TFT LCD Panel:



6.2、PCB size: 100.0(W)×60.15(H) ×9.2(D) mm



7、Product logo:

HE070NA-13B-27H

8、Packing、Transport and Storage

1、Packing

TBD

2、Transport and store

Don't hit and rain when transportation; Don't storage with chemical goods and wet goods together.

9、JD567T123+6124PNotice

1. The TFT products were had precision testing and ageing test with the instrumentation before transport, so as it no need to adjust again.
2. Before adjusting, should connecting the power, video signal, and switch power correctly.
3. The products is electronics product, please attention for anti-static.
- 4、7"TFT - LCD PANEL is made of glass, take it careful to avoid break.
- 5、Adjust the potentiometer should pay attention to not touch the button pin, because the human body has a certain resistance, such as touch will affect the key function.

10、 7.0 "TFT - LCD PANEL Inspection Standard:

Aim : Make the panel standards to material purchasing, process inspecting and customer checking.

Ranges:: 7"TFT LCD

Content:

10.1 Determinant standard and method:

10.1.1. The method and determinant of inspecting the nick of panel of LCD:

10.1.1.1. Inspect vertically (or at 45°angle from left/right) under the light tube (the power is 20 W) in the distance of 30cm to the panel. If there is no nick, it determines "OK", otherwise "NG".

10.1.2、 LCD display black spots, white spots, color point detection methods and judgments:

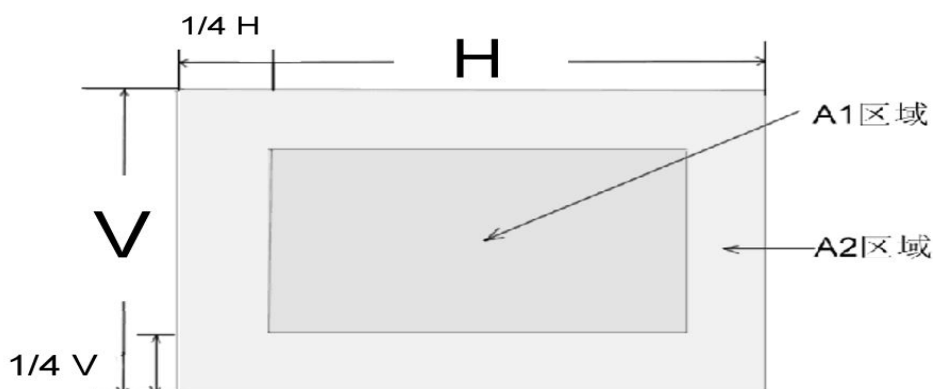
10.1.2.1. Inspecting method

10.1.2.1.1. Black spots: under the situation of "turn on the light", set the MASK of

black spot inspection near the black spot then compare the big and small by eyes.

10.1.2.1.2. White & Color spots: under situation of "turn on the light", set the Mask of black spot inspection on the white spot (or color spot) then observe them by eyes if it can hide.

10.1.2.2. Display area division



Remark: Area of A1: The center of the available area for the picture

Area of A2: The edge of the available area for the picture(around the area)

10.1.3 Determinant Choice:

Spot Diameter (mm)		Allowed Area	
		A 1	A 2
Black Spot	$d \leq 0.15$	Negate	Negate
	$0.15 < d \leq 0.3$	4	4
	$0.3 < d \leq 0.5$	2	3
	$0.5 < d < 0.8$	0	2
White or color spot	$d \leq 0.15$	Negate	Negate
	$0.15 < d \leq 0.3$	3	3
	$0.3 < d \leq 0.5$	1	2
	$0.5 < d < 0.8$	0	1

- Remark:
1. Size: Average Diameter= (Max. Diameter + Min. Diameter) /2
 2. Using information above as a standard in order to judge while the e spots a dense.
 3. Black & White spot: To judge the obvious spots through the change of voltage by comparison.
 4. Total quantity of Black & white & color spot: $A1+A2 \leq 4$.